



PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Masaru FUKU, et al.

Appln. No. 09/419,787

Group Art Unit: To be assigned

Confirmation No.: Unknown

Examiner: J. MITCHELL

Filed: October 18, 1999

For: ONBOARD SEMICONDUCTOR DEVICE

EXCESS CLAIM FEE PAYMENT LETTER

Commissioner for Patents
Washington, D.C. 20231

Sir:

An Amendment Under 37 C.F.R. § 1.111 is attached hereto for concurrent filing in the above-identified application. The resulting excess claim fee has been calculated as shown below:

	After Amendment		Highest No. Previously Paid For					
All Claims	6	-	20	=	X	\$18.00	=	\$.00
Independent	6	-	5	=	1 X	\$84.00	=	\$84.00
TOTAL							=	\$84.00

A check for the statutory fee of \$84.00 is attached. Please charge any additional fee or credit any overpayment to our Deposit Account No. 19-4880. A duplicate copy of this letter is enclosed.

Respectfully submitted,

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Date: February 8, 2002



PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Masaru FUKU, et al.

Appln. No.: 09/419,787

Confirmation No.: Unknown

Group Art Unit: 2822

Filed: October 18, 1999

Examiner: J. MITCHELL

For: ONBOARD SEMICONDUCTOR DEVICE

AMENDMENT UNDER 37 C.F.R. § 1.111

Commissioner for Patents
Washington, D.C. 20231

Sir:

In response to the Office Action dated November 8, 2001, please amend the above-identified application as follows:

IN THE CLAIMS:

Please add the following new claim:

-
7. An onboard semiconductor device, comprising:
- a power chip circuit board on which a power chip is mounted;
- a control circuit board having mounted thereon an electrical component in relation to said power chip; and
- an outer case in which said power chip circuit board and the control circuit board are contained,

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